

PTO-1449  <div style="border: 1px solid black; border-radius: 50%; padding: 10px; display: inline-block; transform: rotate(-15deg);">           INFORMATION DISCLOSURE CITATION            in an Application            MAR 16 2005            PATENT &amp; TRADEMARK OFFICE         </div>		Application No. 09/870,447 Docket Number 068354.1471		Applicant(s) Brian Boles et al. Group Art Unit 2111 Filing Date June 1, 2001		
<b>U.S. PATENT DOCUMENTS</b>						
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
TSC A.	3930253	12/30/75	Maida	340	347	1/24/74
TSC B.	5974549	10/26/99	Golan	713	200	3/27/97
TSC C.	6055619	4/25/00	North et al.	713	36	2/7/97
TSC D.	6282637	8/28/01	Chan et al.	712	223	12/2/98
TSC E.	6728856	4/27/04	Grosbach et al.	711	202	6/1/01
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<del>K.</del>	<del>01037424 A</del>	<del>2/89</del>	<del>JP</del>	<del>H03M</del>	<del>001/82</del>	<del>X</del>
TSC L.	0 554 917 A2	8/11/93	EP	G06F	9/26	X
TSC M.	0 855 643 A1	7/29/98	EP	G06F	9/30	X
TSC N.	0 992 888	12/4/00	EP	G06F	9/32	X
TSC O.	0 992 889	12/14/00	EP	G06F	9/32	X
TSC P.	96/11443	4/18/96	WO	G06F	15/78	X
<b>NON-PATENT DOCUMENTS</b>						
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
TSC Q.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508					5/2001
TSC R.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, IS, vol. 37, no. 10, pages 46, 48, 50-53, XP000860706 ISSN:0010-4566					10/1998
TSC S.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515					3/2/1998
TSC T.	Intel, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, , Pages 3-1, 3-2, 3-15, 14-1 to 14-30, 18-7, and 25-289 to 25-292					1995
TSC U.	Intel, Embedded Intel486 Processor Family Developer's Manual, pgs. 2-2, 3-17, 3-37, 4-5, 4-6, 10-1 to 10-12, 12-1 to 12-10					10/1997
EXAMINER 				DATE CONSIDERED 10-June-05		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						

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A.						
B.						
C.						
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TSC Q.	PCT Search Report based on PCT/US02/16706, 6 pages	Mailed 9/27/02
TS R.	PCT Search Report based on PCT/US02/16705, 7 pages	Mailed 9/9/02
TSC S.	PCT Search Report based on PCT/US02/16921, 4 pages	Mailed 10/18/02
TS T.	SPARC, International, Inc., "The SPARC Architecture Manual", Version 8, pp 1 - 303	1992
TS U.	Weaver, et al., SPARC International, Inc. "The SPARC Architecture Manual", Version 9, pp. xiv, 137, 146-147, 200-204, 221-222, 234-236, 299	1994-2000
TS V.	Free On-Line Dictionary of Computing (FOLDOC). <a href="http://wombat.doc.ic.ac.uk/foldoc/">http://wombat.doc.ic.ac.uk/foldoc/</a> Search term: program counter	1995

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TSC B.	5973527	10/26/99	Schweighofer et al.	327	172	3/9/98
TSC C.	6002234	12/14/99	Ohm et al.	318	729	3/9/98
TSC D.	6434020	8/13/02	Lambert et al.	363	17	4/9/01
TSC E.	6552625	4/22/03	Bowling	332	109	6/1/01
TSC F.	6643150	11/4/03	Kawakami	363	41	3/5/02
TSC G.	6724169	4/20/04	Majumdar et al.	318	811	10/1/02
TSC H.	20040150439	8/5/04	Greenfeld	327	131	2/3/03
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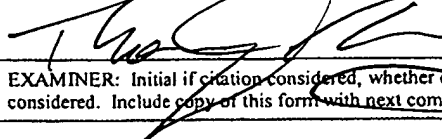
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